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PATENT
091-0124

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Daniel N. Harres et al. : Confirmation No.: 9774
Serial No: 10/061,517 : Group Art Unit: 2133
Filed: June 20, 2002 : Examiner: James C. Kerveros

For: BUILT-IN TEST SIGNAL
ATTENUATION CIRCUIT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AUG 24 2004

Technology Center 2100

AMENDMENT AND RESPONSE

Sir:

In response to the Office Action dated as mailed on June 17, 2004, and having a period of response extending through and including September 17, 2004, please make the below-identified amendments and consider the following remarks:

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this paper.

Amendments to the Drawings begin on page 8 of this paper and includes attached replacement sheets.

Remarks/Arguments begin on page 9 of this paper.

An **Appendix** including amended drawing figures is attached following page 14 of this paper.